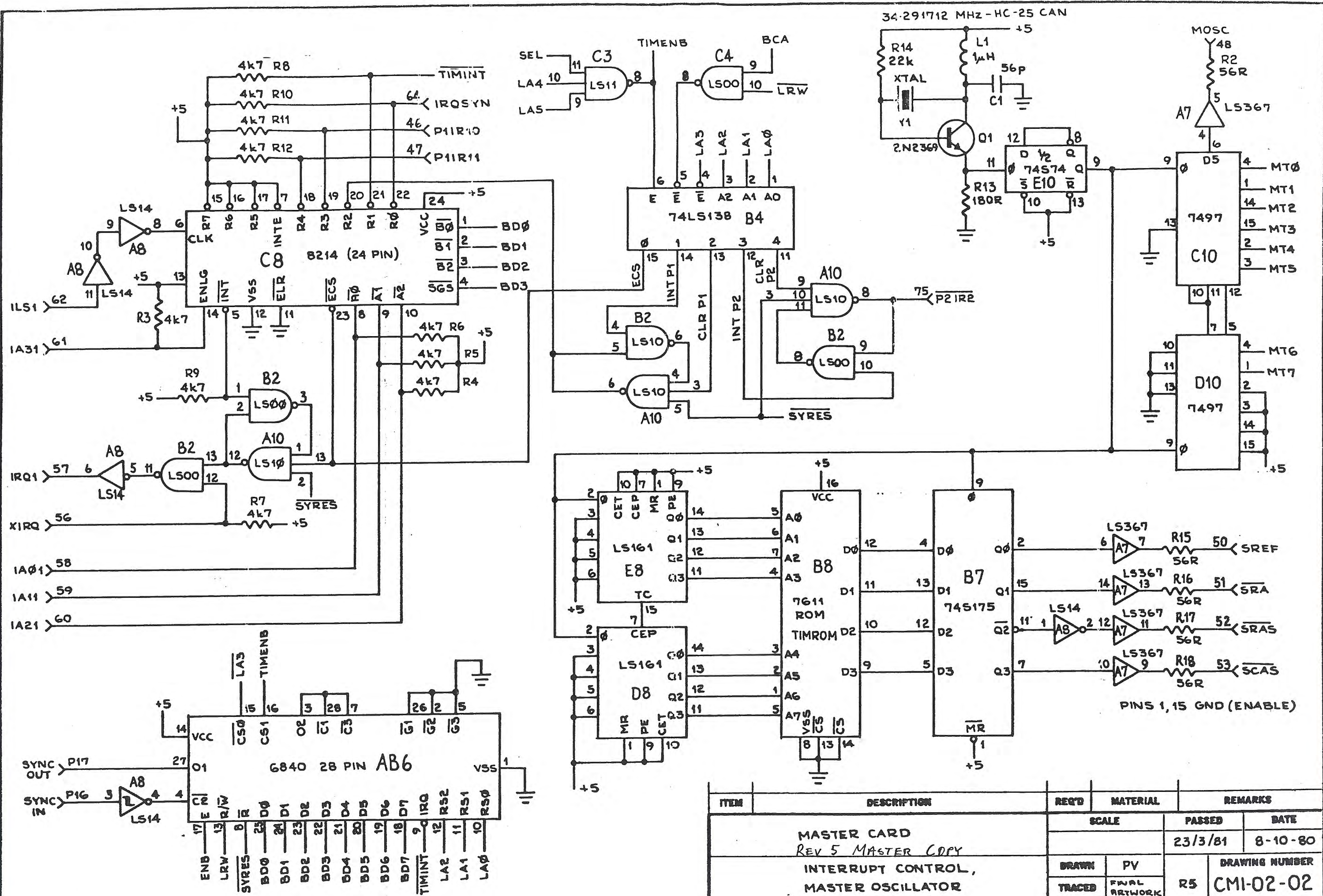
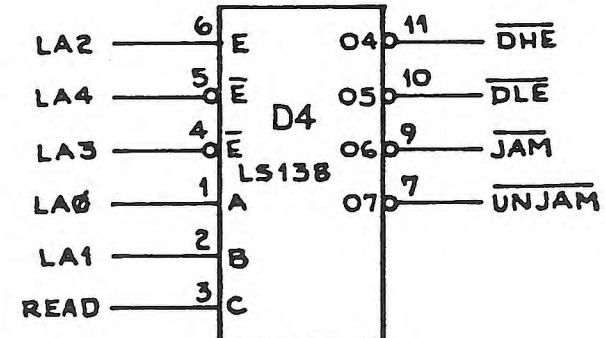
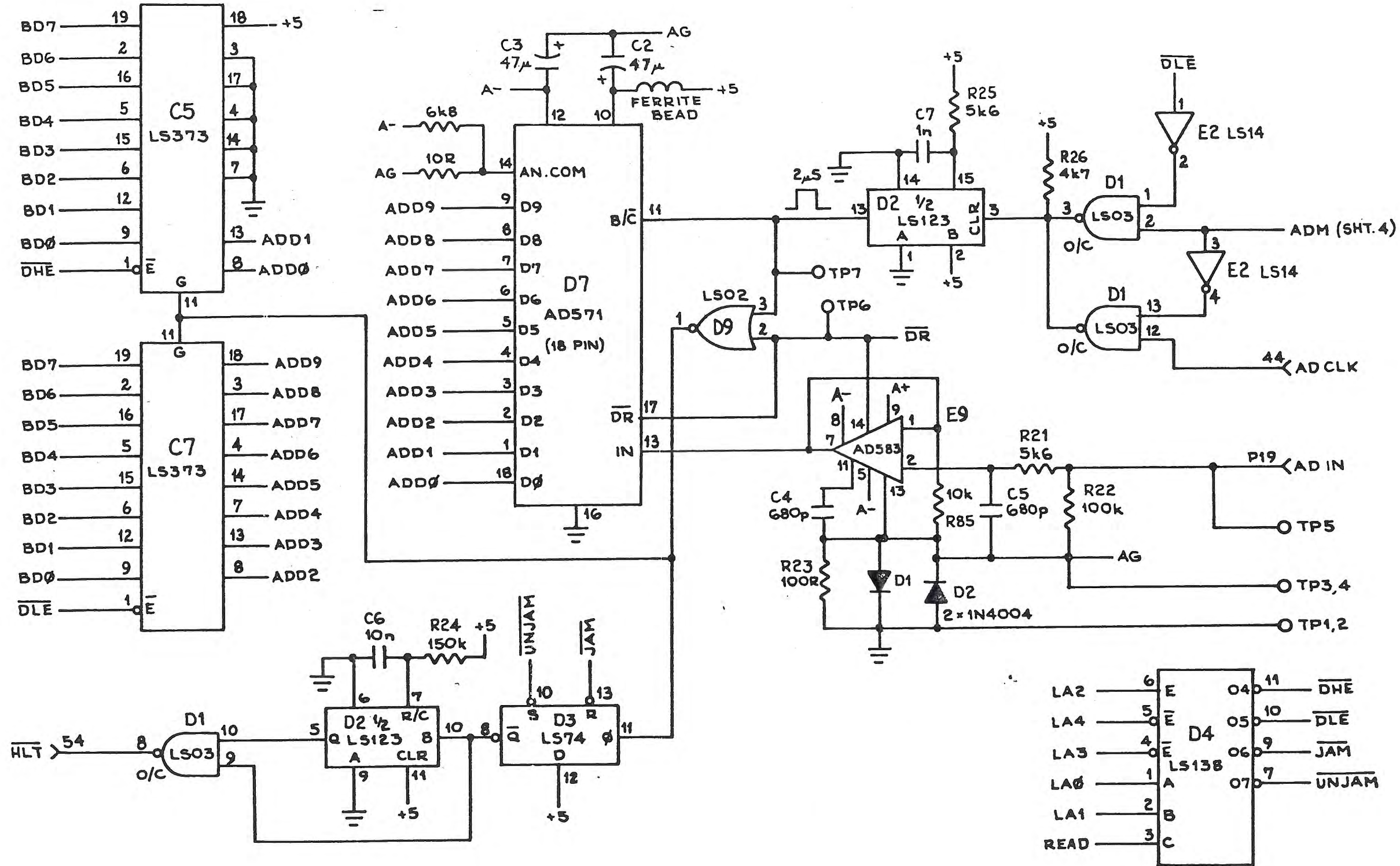


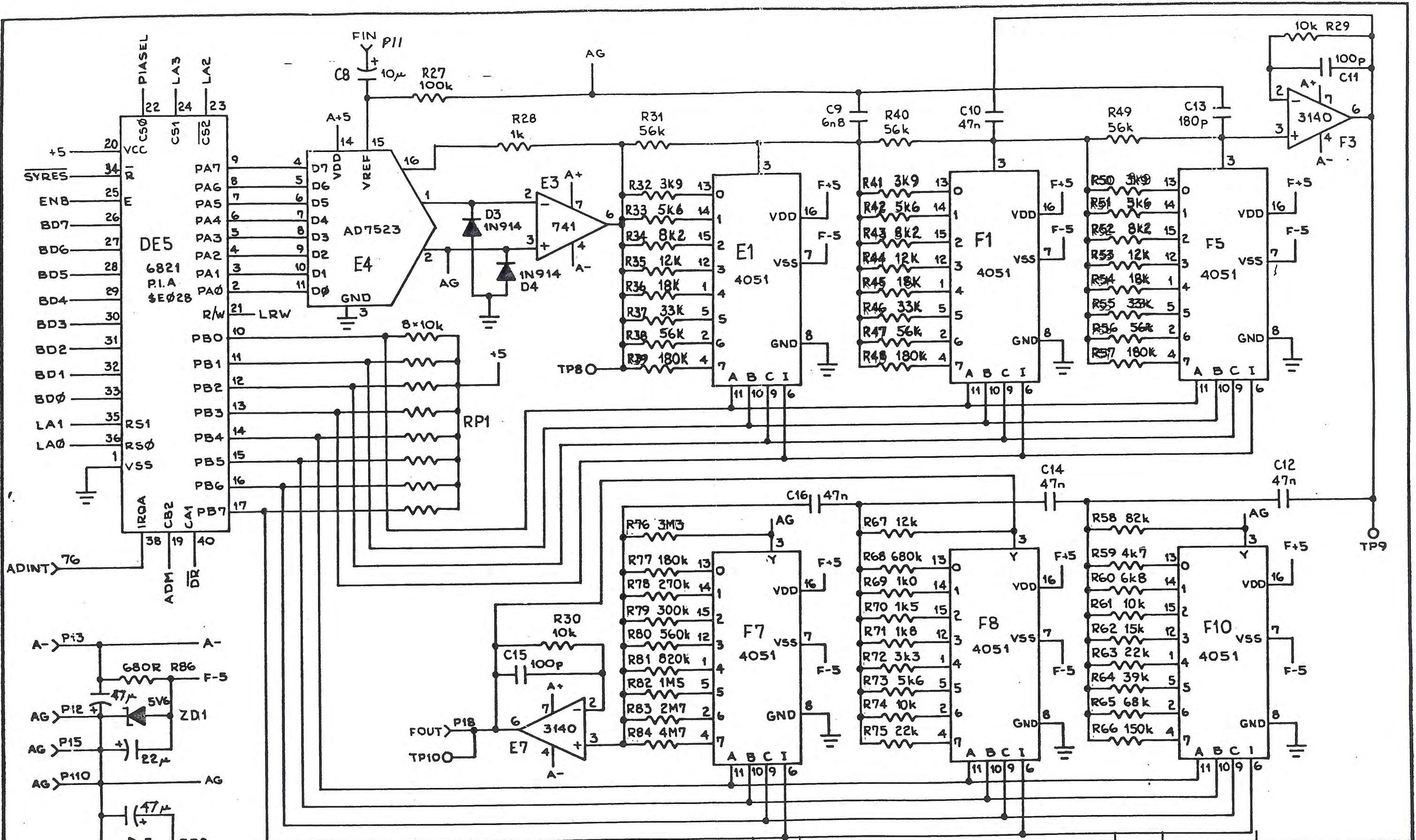
ITEM	DESCRIPTION	REQ'D	MATERIAL		REMARKS	
			SCALE	PASSED		DATE
MASTER CARD REV 5 MASTER COPY					23/3/81	8-10-80
ADDRESS DECODING; CHANNEL SELECTION RAM SWITCHING; MASTER TUNING REGISTER					DRAWN PV TRACED FINAL NETWORK CHECKED PV	R5 DRAWING NUMBER CM1-02-01 SHT 1 of 4



ITEM	DESCRIPTION	REQ'D	MATERIAL	REMARKS
MASTER CARD REV 5 MASTER COPY		SCALE		PASSED 23/3/81
INTERRUPT CONTROL, MASTER OSCILLATOR MEMORY CONTROL		DRAWN PV		DATE 8-10-80
		TRACED FWAL ARTWORK		DRAWING NUMBER R5 CM1-02-02
		CHECKED PV		SHT 2 of 4



ITEM	DESCRIPTION	REQ'D	MATERIAL	REMARKS
	MASTER CARD REV 5 MASTER COPY		SCALE	PASSED 23/3/81
	AD CONVERTER		PV	DATE 7-10-80
			TRACED	DRAWING NUMBER R5 CMI-02-03
			CHECKED	SHT 3 of 4



ITEM	DESCRIPTION	REQ'D	MATERIAL	REMARKS
MASTER CARD REV 5 MASTER COPY				SCALE
A-D FILTER SYSTEM				PASSED
				DATE
				DRAWN
TRACED	FINAL NETWORK	R5	CM1-02-04	
CHECKED	PV		SHT 4 of 4	

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FAIRLT AA27998
GA
5122278+
22278 BYCO G

FAIRLT AA27998

26/9/84

ATTN: KEN MC ALPINE

RE: CMI-D2 REV 4 FIELD CHANGE FOR 6809 UPGRADE

1. ISOLATE PIN 1 OF D2 (CUT TRACK APPEARING BETWEEN PINS 13 AND 14 OF D2 ON COMPONENT SIDE) AND CONNECT TO +5V.
2. PULL OUT PIN 7 OF A1 AND CONNECT TO PIN 6 OF D2.
3. CUT TRACK ON COMP. SIDE TO PIN 6 OF D3 APPEARING BETWEEN PINS 9 AND 10 OF D2.
4. CONNECT PIN 6 OF D3 TO +5V.
5. CUT TRACK APPEARING BETWEEN PINS 12 AND 13 OF D2 ON COMPONENT SIDE.
6. CONNECT 3K9 PULLUP RESISTOR BETWEEN PIN 2 OF D2 AND PIN 14 OF D1 (+5V).
7. ISOLATE PIN 63 OF EDGE CONNECTOR: CUT TRACK APPEARING BETWEEN PINS 12 AND 13 OF D3 ON COMPONENT SIDE AND CUT TRACK JOINING PINS 3 AND 4 OF D3 ON SOLDER SIDE.
8. CONNECT PLATE-THRU HOLE JOINED TO EDGE CONNECTOR 63 TO PIN 2 OF D2.
9. MARK BOARD REV. 6.

REGARDS
QUENTIN GOLDFINCH

22278 BYCO G
FAIRLT AA27998